Electronic Counters

10 mHz to 18 GHz

R5372P

- Measurement Range: 10 mHz to 18 GHz
- Unique Method Enables Pulse Modulation Measurements with 50 ns Pulse Width and 10 kHz Resolution
- Pulse-Width Measurement to 10 ns Resolution
- Enhanced Range of Calculation Functions
- Automatic Measurement of Maximum FM Deviation

R5372P Microwave Frequency Counters

R5372P is high-resolution microwave frequency counters capable of pulse modulation measurements up to 18 GHz. Not only are measurements of aircraft, harbor, river and fire prevention radar possible but also, these counters are capable of CW microwave frequency measurements, totalizing functions and PCM error-bit totalizing. Using the A input which makes use of a reciprocal counting technique (10 mHz to 10 MHz), it is possible to perform pulse width or pulse repetition frequency measurements to a resolution of 10 ns. By using an external trigger signal, it is possible to measure a pulse modulated carrier frequency at any desired point (frequency profile measurement).

These counters also provide high basic performance, typified by an input sensitivity of −20 dBm at 18 GHz and 1 Hz resolution with a 1 s gate using a digital TRAHER technique. In addition, plus/minus offset display is possible using keyboard entry of any or all digits, as are digital comparison functions, arithmetic calculation result display and display of maximum values, minimum values, standard deviation and parts per million – all provided as standard.

By building in an optional GPIB or BCD output capability, these counters can be used in automated microwave measurement systems.

- High-Resolution Measurement of Pulse Modulated Signals
  To measure pulse modulated signals, the frequency counter’s gate time must be set at or within the pulse width. The result with conventional counters is that the readout accuracy suffers. The R5372P microwave counters on the other hand, with their on-board CPUs and true averaging function can display three additional digits. Only the resolution need to be set manually; gate time and number of averaging operations are automatically set. The minimum measurable pulse width for pulse modulated signals is 100 ns using an internal gate signal or 50 ns using an external gate signal.

- Frequency Measurement at any Desired Portion of a Pulse Modulated Carrier
  By inputting a TTL-level, negative logic, external gate signal at the rear-panel connector; the carrier frequency of a chirp radar can be measured at any desired frequency or pulse width and provide a pulse modulated carrier frequency profile. Pulse widths of 50 ns to 0.1 s can be measured. The resolution of the measured frequency is set using the RESOLUTION keys on the front panel.

- Reciprocal Method Enables Pulse Width Measurement
  Signal input at the A input (10 mHz to 10 MHz) is processed by taking the reciprocal of the frequency to provide rapid, high-resolution measurement of the pulse width and the pulse repetition frequency of a pulse modulated signal. The measurement procedure is simple; just connect the rear-panel signal detection output to input A. Applications include measurement of the pulse width and repetition frequency of radar signals and of two-way radio pilot signals. DC coupling allows PCM error bit accumulation as well.

- FM Deviation Measurements Are Simple
  FPU and STL testing of TV relay equipment requires measurements of transmitted output and frequencies as well as FM deviation measurements. The ∆F mode of the R5372P can be used to perform easily deviation measurements of FM modulated frequencies and by means of an external trigger signal is also possible.
■ Pulse Modulated Signal Measurement
The R5372P microwave counters feature straightforward operating procedures to measure the important characteristics of radar and other pulse modulated signals. The examples below illustrate measurement of a carrier signal at 9450 MHz with a pulse repetition frequency of 3210 Hz and pulse width of 100 ns.

1. Carrier Frequency Measurement
With the R5372P in the manual mode, set the frequency to 9450.5 MHz, approximating the frequency to be measured (within 9450.5 ± 125 MHz) and set the sample rate to HOLD. Switch the sync mode to internal (rear panel INT. connector) so that if the first gate time value is too short to obtain the required resolution, internal averaging will be performed automatically.

2. Repetition Frequency Measurement
After performing the same procedure as for carrier frequency, use either the detected output from the AUX IN/OUT connector at the rear panel or provide a signal from an external detector. Input this signal to the input A connector of the R5372P. Set the input A to the 10 mHz to 10 MHz measurement band (reciprocal mode) to obtain a measurement result of 3210.2 Hz in approximately 3 ms with high resolution. This mode overrides the hold on the sampling rate.

3. Pulse Width Measurement
To measure pulse width, set up the counter as for carrier frequency measurement and press the SHIFT and PW keys to obtain a measurement with 10 ns resolution. When measuring pulse modulated signals having a long rise time, the displayed pulse width may exceed the actual pulse width because the pulse width is measured by measuring the threshold level width of the counter. In such cases, connect the output of a detector to input A and adjust the trigger level to enable measurement of the 50% pulse width.

■ FM Deviation Measurement
The R5372P’s powerful arithmetic functions make it a simple task to measure FM deviation. Connect the counter as shown in the figure below and select the ∆F mode. It is then possible to simply turn the delay knob to determine the maximum and minimum values from the start of measurement and internally calculate the differences later. An external gate signal (50 ns to 0.1 s) synced at any desired amplitude with the modulated signal can also be used for high-accuracy measurement of FM deviation (the frequency change vs amplitude in frequency modulated signals). Specifically, when the required resolution cannot be obtained due to reduced external gate time (50 ns min.), the averaging mode can be used to obtain high-resolution measurements. This is another application of the enhanced arithmetic functions of these counters.

Measurement of FM Deviation

Measurement of Pulse Modulated Wave

Frequency measurement at desired point in pulse by external gate (resolution set at panel)
# Electronic Counters

## 10 MHz to 18 GHz

### RS372P (Continued From Previous Page)

### Specifications

<table>
<thead>
<tr>
<th>Input</th>
<th>Input A</th>
<th>Input B</th>
</tr>
</thead>
<tbody>
<tr>
<td>Frequency measurement range</td>
<td>10 MHz to 10 MHz (DC coupled), 10 MHz to 10 MHz (AC coupled)</td>
<td>10 MHz to 550 MHz</td>
</tr>
<tr>
<td>Input impedance</td>
<td>Approx. 1 MΩ/50 Ω max.</td>
<td>Approx. 50 Ω</td>
</tr>
<tr>
<td>Input sensitivity</td>
<td>2 mVrms</td>
<td>25 mVrms</td>
</tr>
<tr>
<td>Input attenuator</td>
<td>0 dB to 20 dB</td>
<td>ANR</td>
</tr>
<tr>
<td>Maximum measurement input</td>
<td>500 mVrms±0.1 dB</td>
<td>500 mVrms/ANS-OFF</td>
</tr>
<tr>
<td>Damage level input</td>
<td>6 Vrms (1 MHz to 10 MHz)</td>
<td>10 Vrms (500 kHz to 1 MHz)</td>
</tr>
<tr>
<td>Coupling</td>
<td>DC-N type</td>
<td>DC-N type</td>
</tr>
<tr>
<td>Trigger level</td>
<td>Approx. ±1 V from continuously variable (when –0 V to +5 V input 20 Ω)</td>
<td>–</td>
</tr>
<tr>
<td>Resolution/counting time</td>
<td>See Fig. 1</td>
<td>10 MHz/0.1 μs to 0.1 Hz/10 s decade switched</td>
</tr>
<tr>
<td>Measurement accuracy</td>
<td>±1 count ± time base accuracy</td>
<td>±1 count ± time base accuracy</td>
</tr>
<tr>
<td>Measurement method</td>
<td>Reciprocal</td>
<td>Direct counting</td>
</tr>
</tbody>
</table>

### Input connector: BNC

### Pulse width: 100 ns to 1 s (internal sync)

50 ns to 0.1 s (external sync)

### Pulse repetition frequency \( f_R \): 10 Hz to 5 MHz

#### Synchronizing trigger modes:

- **INT.** Internal synchronizing. Gate is opened and closed in sync with a pulse modulation of the input.
- **EXT. START** Gate is opened when an external start signal is received. Triggering is only possible when an internal detector output is on.
- **EXT. GATE** Gate opens and closes when an external gate signal is received.

**Gate input signal level:** TTL, negative logic

**Pulse width:** 50 ns to 0.1 s

**LINE** Gate opens in sync with power-line frequency. Triggering is only possible when an internal detector output is on.

### Carrier frequency measurement:

#### Maximum resolution: See Fig. 2

#### Accuracy

\[ \text{Accuracy} = \frac{1}{(\text{Resolution}) \times \text{GW}} \]

\[ \text{Accuracy} \approx 0.04 \frac{1}{(\text{Hz rms})} \]

\[ \text{Accuracy} \approx 0.04 \frac{1}{(\text{kHz})} \]

**Measurement time:**

\[ (50 \mu s + \frac{1}{f_R}) \times \text{Resolution} \times \text{GW} \]

**GW:** Gate width (for internal synchronizing, this is the modulation pulse width minus approx. 50 ns)

**\( f_R \):** Pulse repetition frequency

**Units display:** GHz, kHz, Hz, fixed display

**Modulation pulse width measurement:**

Pulse width display at the counter input sensitivity level

**Resolution:** 10 ns

**Accuracy:** ±30 ns ±5 mV base accuracy

**Units:** µs fixed display

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### Figure 1: Measuring Time, Resolution and Number of Cycles vs Input Frequency

<table>
<thead>
<tr>
<th>Input frequency (Hz)</th>
<th>Resolution (µs)</th>
</tr>
</thead>
<tbody>
<tr>
<td>10 MHz</td>
<td>100</td>
</tr>
<tr>
<td>100 kHz</td>
<td>10</td>
</tr>
<tr>
<td>1 MHz</td>
<td>1</td>
</tr>
<tr>
<td>100 kHz</td>
<td>0.1</td>
</tr>
<tr>
<td>10 kHz</td>
<td>0.01</td>
</tr>
<tr>
<td>100 kHz</td>
<td>0.001</td>
</tr>
</tbody>
</table>

**Maximum resolution:** MSD 1 to 2: 9-digit display

**Measurement range:**

- 10 MHz to 10 MHz (DC coupled)
- 10 MHz to 10 MHz (AC coupled)
- 10 MHz to 550 MHz
- 500 MHz to 18 GHz

**Units:** µs (fixed display)

**Trigger error:** ±1 count ± time base accuracy

- Signal slope (V/µs) (µs)
- Peak noise voltage (V/µs)

**Pulse Modulation Measurement (MANUAL mode)**

**Measurement range:**

- 10 MHz to 550 MHz (INPUT A)
- 500 MHz to 18 GHz (INPUT B)

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### Diagram

[Diagram of Measuring Time, Resolution and Number of Cycles vs Input Frequency]

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### Notes

- **Trigger error:** ±0.5% with respect to an a-sine wave input of 40 dB or higher SN
**Electronic Counters**

**Automatic Measurement of Pulse Microwave Signals**

**R5372P**

**Power consumption:**
120 VA (maximum)

**External dimensions:**
Approx. 255 (W) × 132 (H) × 420 (D) mm

**Mass:**
10 kg (maximum)

**Time Bases**

**Time base stability:**

<table>
<thead>
<tr>
<th></th>
<th>Standard</th>
<th>Option 21</th>
<th>Option 22</th>
<th>Option 23</th>
</tr>
</thead>
<tbody>
<tr>
<td>Aging rate</td>
<td>2 × 10⁻⁸/day</td>
<td>5 × 10⁻⁸/day</td>
<td>2 × 10⁻⁹/day</td>
<td>5 × 10⁻⁹/day</td>
</tr>
<tr>
<td>Long-term stability</td>
<td>1 × 10⁻⁷ yr</td>
<td>5 × 10⁻⁸ yr</td>
<td>2 × 10⁻⁹ yr</td>
<td>5 × 10⁻¹⁰ yr</td>
</tr>
</tbody>
</table>

**Time base output:** Frequency 10 MHz

**Output impedance:** Approx. 50 Ω, BNC connector

**External frequency standard input:** 1, 2, 2.5, 5 or 10 MHz

**Voltage:** 1 to 10 Vp-p

**Input impedance:** Approx. 500 Ω, BNC connector

**Calculation Functions**

- Digital comparator function (limits set using keyboard)
- Maximum & minimum value hold
- Deviation measurement (deviation, maximum, minimum)
- Standard deviation
- Averaging
- PPM
- Offset display, drift display
- Scaling display
- Sum and difference display by automatic measurement of Inputs A and B
- Harmonic frequency display
- Arithmetic functions

**General Specifications**

**Measuring modes (Input B):**

- **AUTO:** A automatic mode with capture time of approx. 300 ms (from reset until count begins)
- FM tolerance 10 MHzp-p (minimum)

**Options**

(These options may be added after delivery of the unit by factory retrofitting.)

- **Option 01:** GPIB interface (IEEE 488-1978)
  - Function: Output of displayed data and remote control of front panel key settings

- **Option 02:** BCD data output
  - Function: Parallel output of the nine least significant digits in binary-coded-decimal form; can be connected to Digital Recorder with 50-pin connector equivalent to Amphenol 57-40500

**Specifications common to Options 01 and 02:**

- **D/A converter analog output:** (From AUX IN/OUT connector)
  - Digits converted: 3 least significant digits
  - Output voltage: -4.995 V to +4.995 V ± 20 mV (+23° ± 5°C)
  - Output impedance: 100 Ω (maximum)

- **Digital comparator output:** (From AUX INPUT/OUTPUT connector) TTL negative logic, open collector

**Option 21, 22 and 23:** Time base oscillator

(See Time Base Stability chart above left)

**Accessories (Sold separately):**

- **R16058** Transit Case
- **A02448** EIA Rack Mount Set
- **A02248** JIS Rack Mount Set

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**Fig. 2: Pulse Width vs Maximum Resolution**